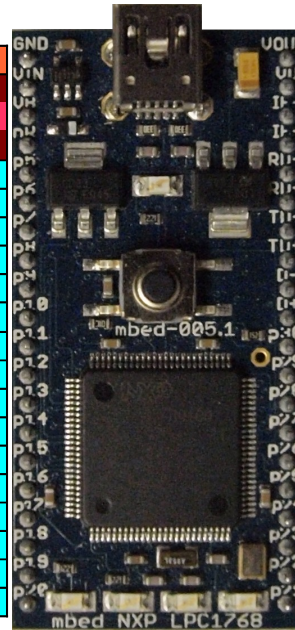


mbed NXP LPC1768



				GND					
				V _{IN} (4½-9V)					
				V _{BAT}					
				/RESET					
3 ↓	2 ↓	1 ↓	0 ↓		V _{OUT} (3.3V)				
TIMER MAT2.3	SSP MOSI1	I2STX_SDA	P0.9	DIP5	V _{USB} (5V)				
TIMER MAT2.2	SSP MIS01	I2STX_WS	P0.8	DIP6	IF-				
TIMER MAT2.1	SSP SCK1	I2STX_CLK	P0.7	DIP7	IF+				
TIMER MAT2.0	SSP SSEL1	I2SRX_SDA	P0.6	DIP8	ETH RD-				
I2C SDA1	UART TXD3	CAN RD1	P0.0	DIP9	ETH RD+				
I2C SCL1	UART RXD3	CAN TD1	P0.1	DIP10	ETH TD-				
SPI MOSI	SSP MOSI0	UART DCD1	P0.18	DIP11	ETH TD+				
SPI MIS0	SSP MIS00	UART CTS1	P0.17	DIP12	USB D-				
SPI SCK	SSP SCK0	UART TXD1	P0.15	DIP13	USB D+				
SPI SSEL	SSP SSEL0	UART RXD1	P0.16	DIP14		0 ↓	1 ↓	2 ↓	3 ↓
TIMER CAP3.0	I2SRX_CLK	ADC AD0.0	P0.23	DIP15	DIP30	P0.4	I2SRX_CLK	CAN RD2	TIMER CAP2.0
TIMER CAP3.1	I2SRX_WS	ADC AD0.1	P0.24	DIP16	DIP29	P0.5	I2SRX_WS	CAN TD2	TIMER CAP2.1
UART TXD3	I2SRX_SDA	ADC AD0.2	P0.25	DIP17	DIP28	P0.10	UART TXD2	I2C SDA2	TIMER MAT3.0
UART RXD3	DAC AOUT	ADC AD0.3	P0.26	DIP18	DIP27	P0.11	UART RXD2	I2C SCL2	TIMER MAT3.1
XXX	ADC AD0.4	V _{BUS}	P1.30	DIP19	DIP26	P2.0	PWM1.1	UART TXD1	XXX
XXX	ADC AD0.5	SSP SCK1	P1.31	DIP20	DIP25	P2.1	PWM1.2	UART RXD1	XXX
					DIP24	P2.2	PWM1.3	UART CTS1	TRACEDATA 3
					DIP23	P2.3	PWM1.4	UART DCD1	TRACEDATA 2
					DIP22	P2.4	PWM1.5	UART DSR1	TRACEDATA 1
					DIP21	P2.5	PWM1.6	UART DTR1	TRACEDATA 0

2 x 2 bits	2 x 4 bits	6 bits	8 bits
P0.0	P0.15	P2.0	P0.4
P0.1	P0.16	P2.1	P0.5
P1.30	P0.17	P2.2	P0.6
P1.31	P0.18	P2.3	P0.7
	P0.23	P2.4	P0.8
	P0.24	P2.5	P0.9
	P0.25		P0.10
	P0.26		P0.11

UART1, UART2, UART3
I2S
SPI, SSP0, SSP1
I2C1, I2C2
CAN1, CAN2
PWM1.[1-6]
TIMER MAT2.[0-3]
TIMER MAT3.[0-1]
TIMER CAP2.[0-1]
TIMER CAP3.[0-1]
ADC0.[0-5]
DAC
TRACEDATA[0-3]

UART1's RI and RTS are missing
 RX pins have two possible locations
 SSP0 and SPI share the same pins
 I2C0 is not available, hence no Fast Mode Plus up to 1MHz
 CAN Spec. v2.0B
 6 single edged or 3 double edged
 output
 output
 input
 input
 12-bit resolution output
 10-bit resolution input
 TRACECLK is missing. mbed-005.1 schematic is wrong.
 It's P2.6, not DIP26, that has TRACECLK.

/ and \ interrupts only on P0.* and P2.*